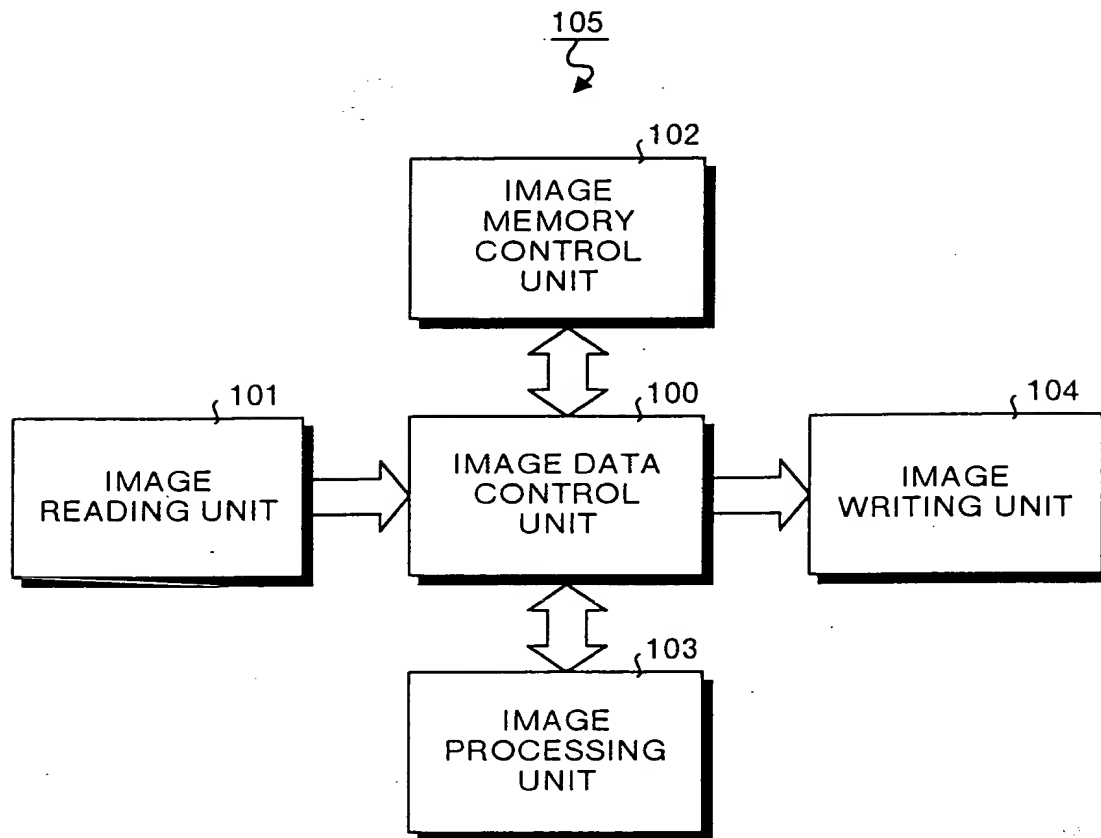
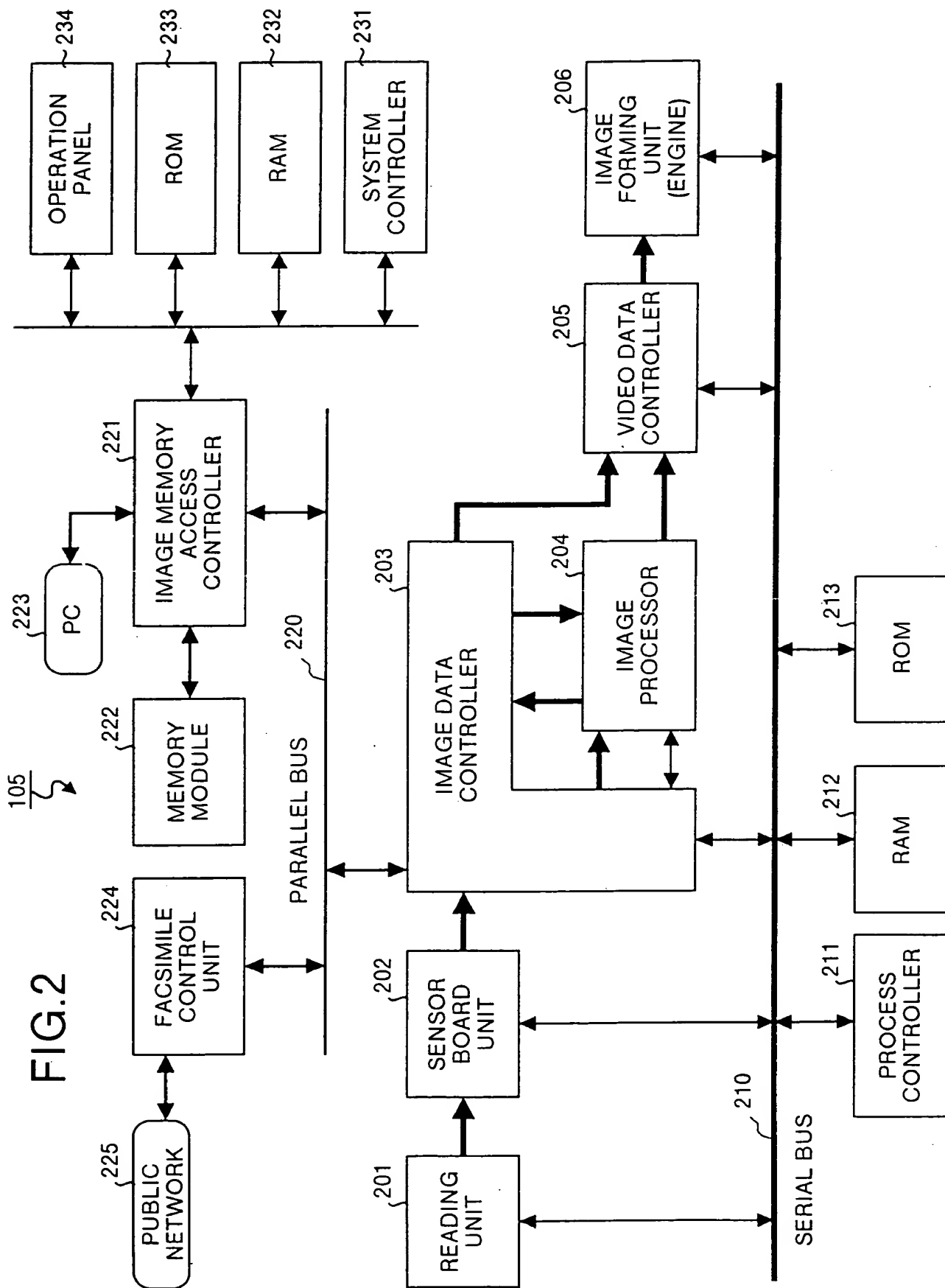


FIG. 1



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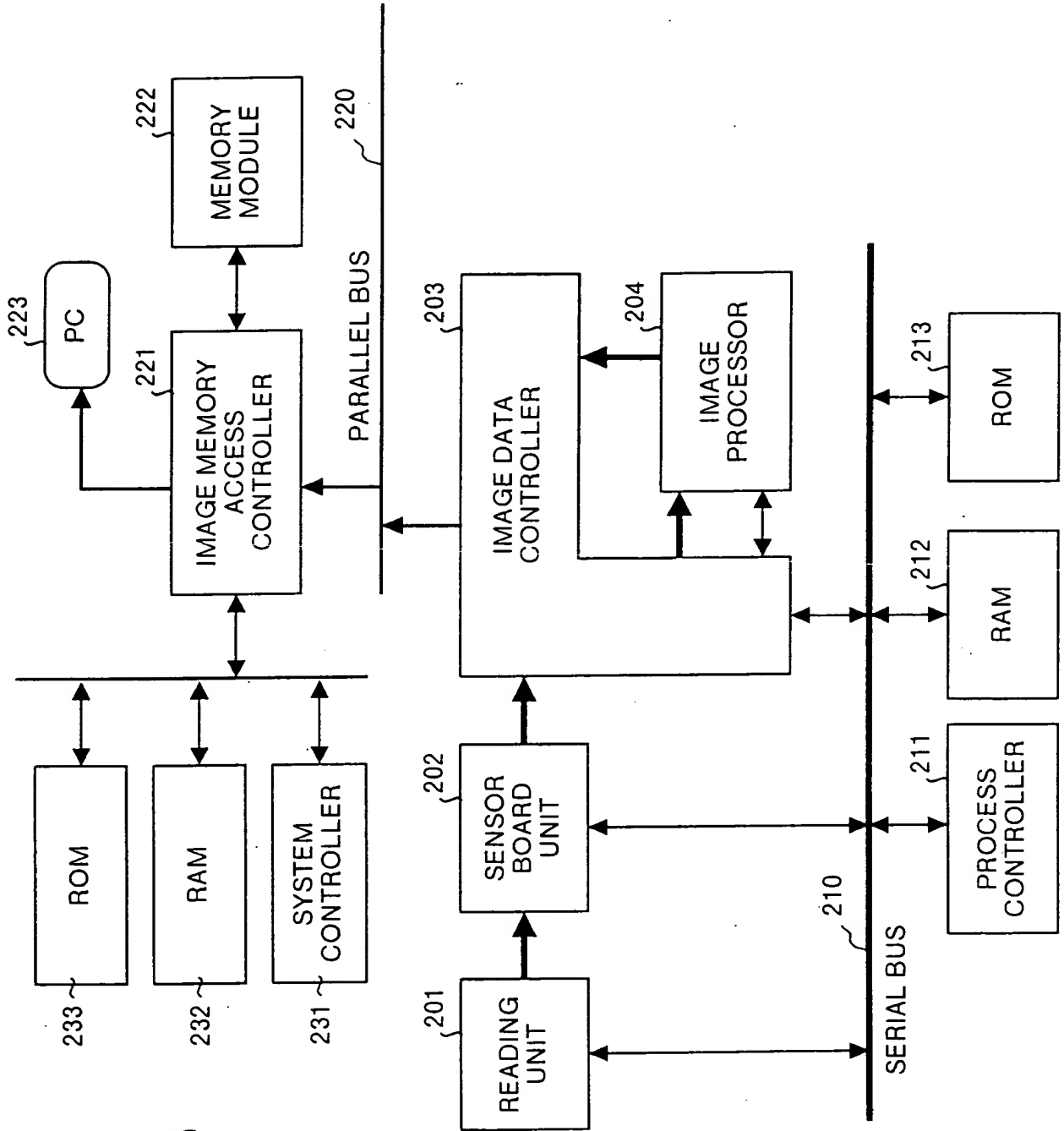
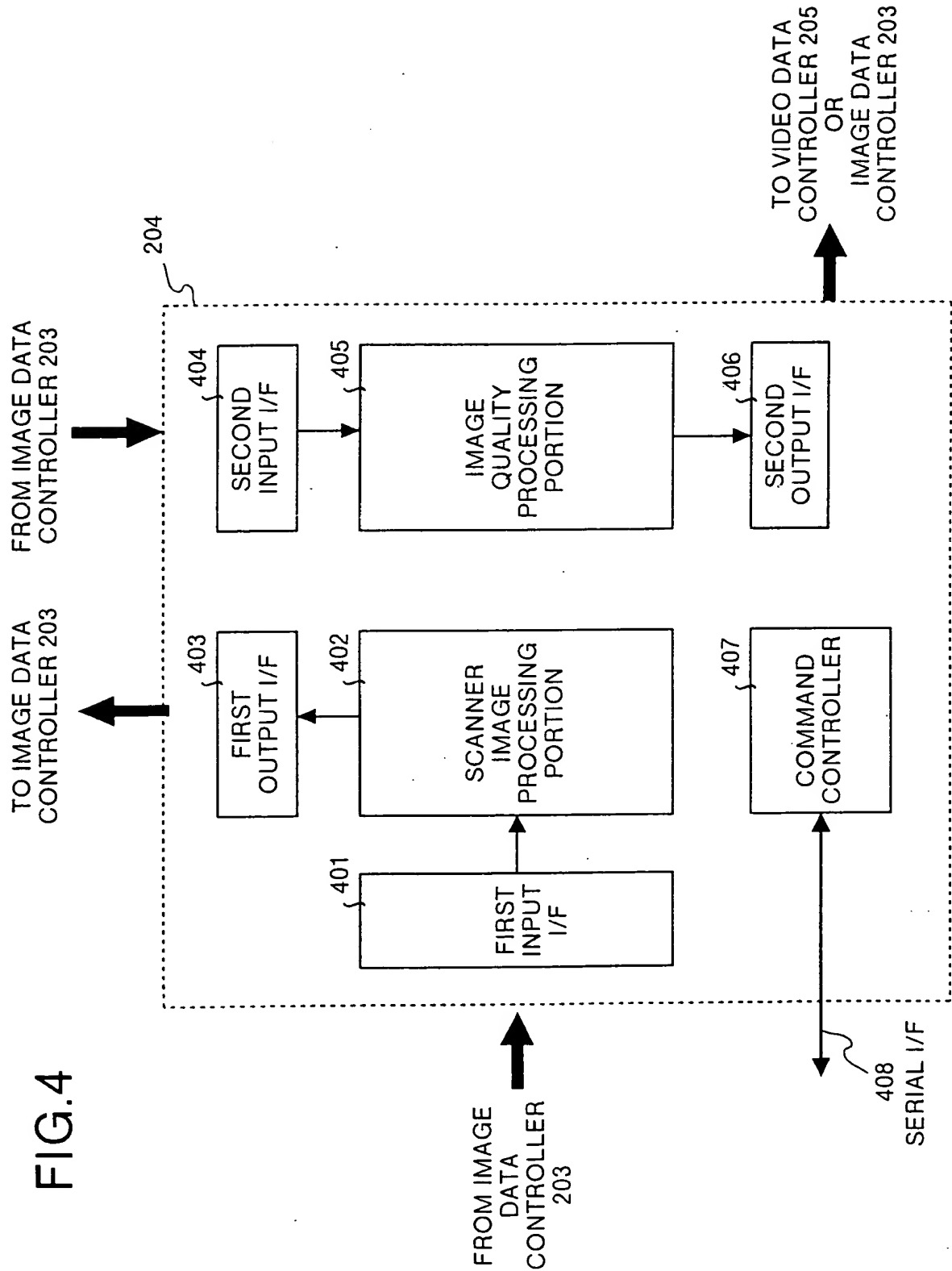


FIG.3

FIG. 3 is a block diagram of a system architecture.

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FIG.5

TO OR FROM IMAGE DATA CONTROLLER 203  
OR  
VIDEO DATA CONTROLLER 205

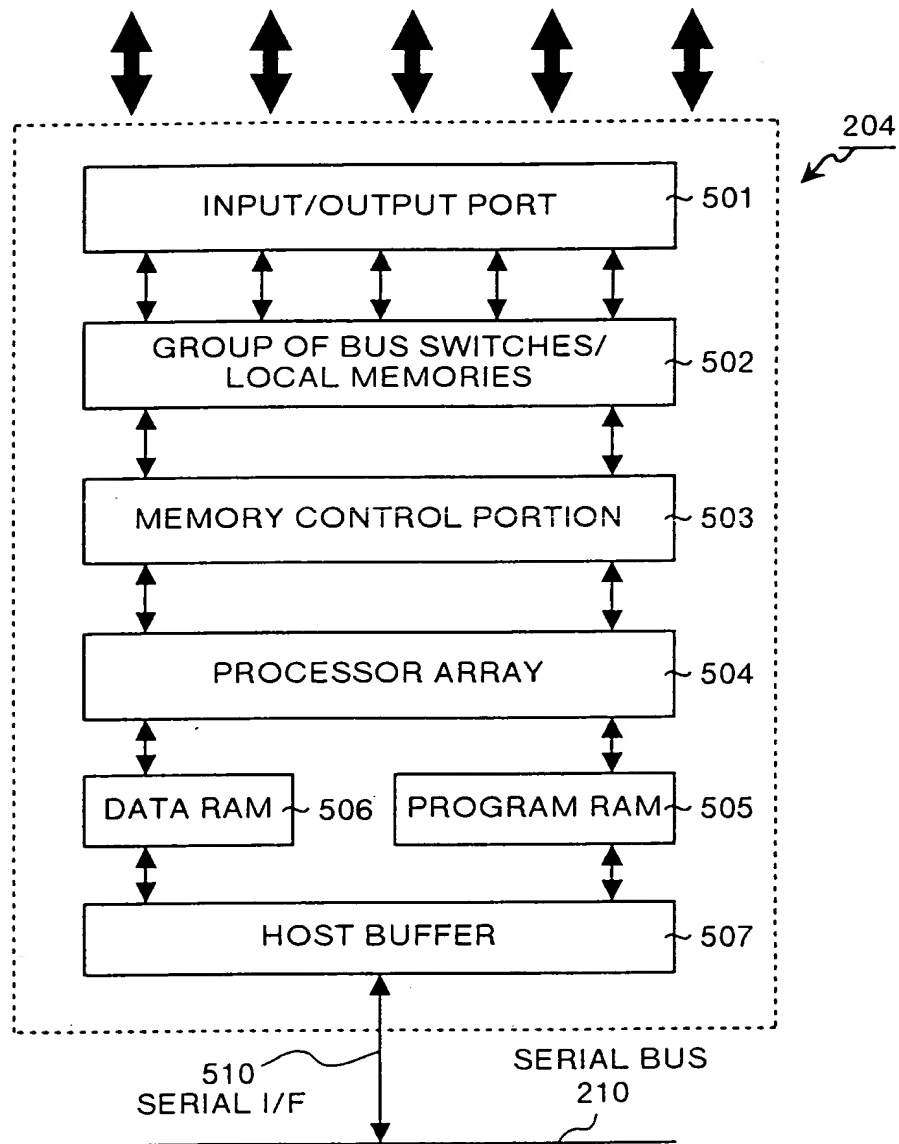
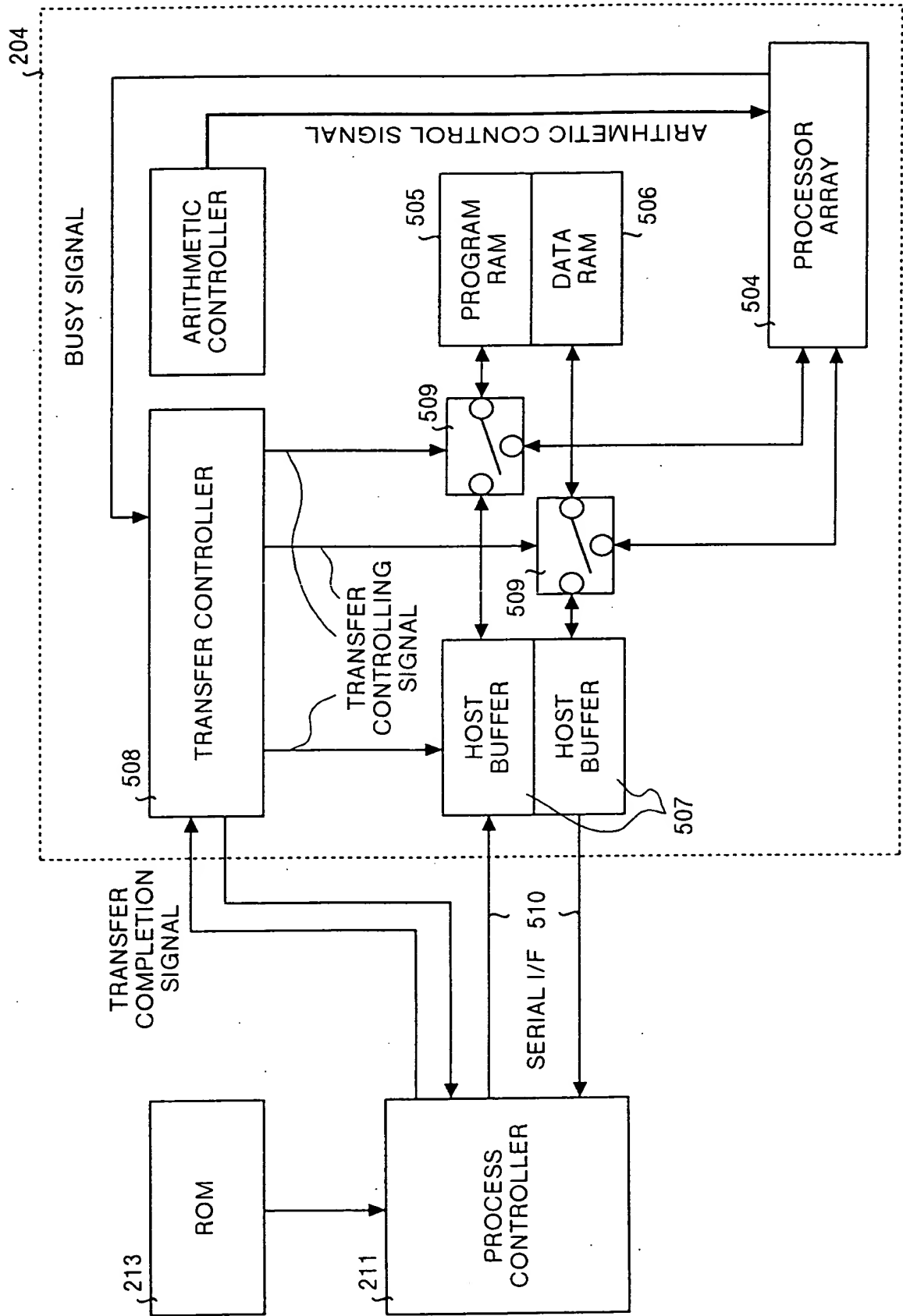
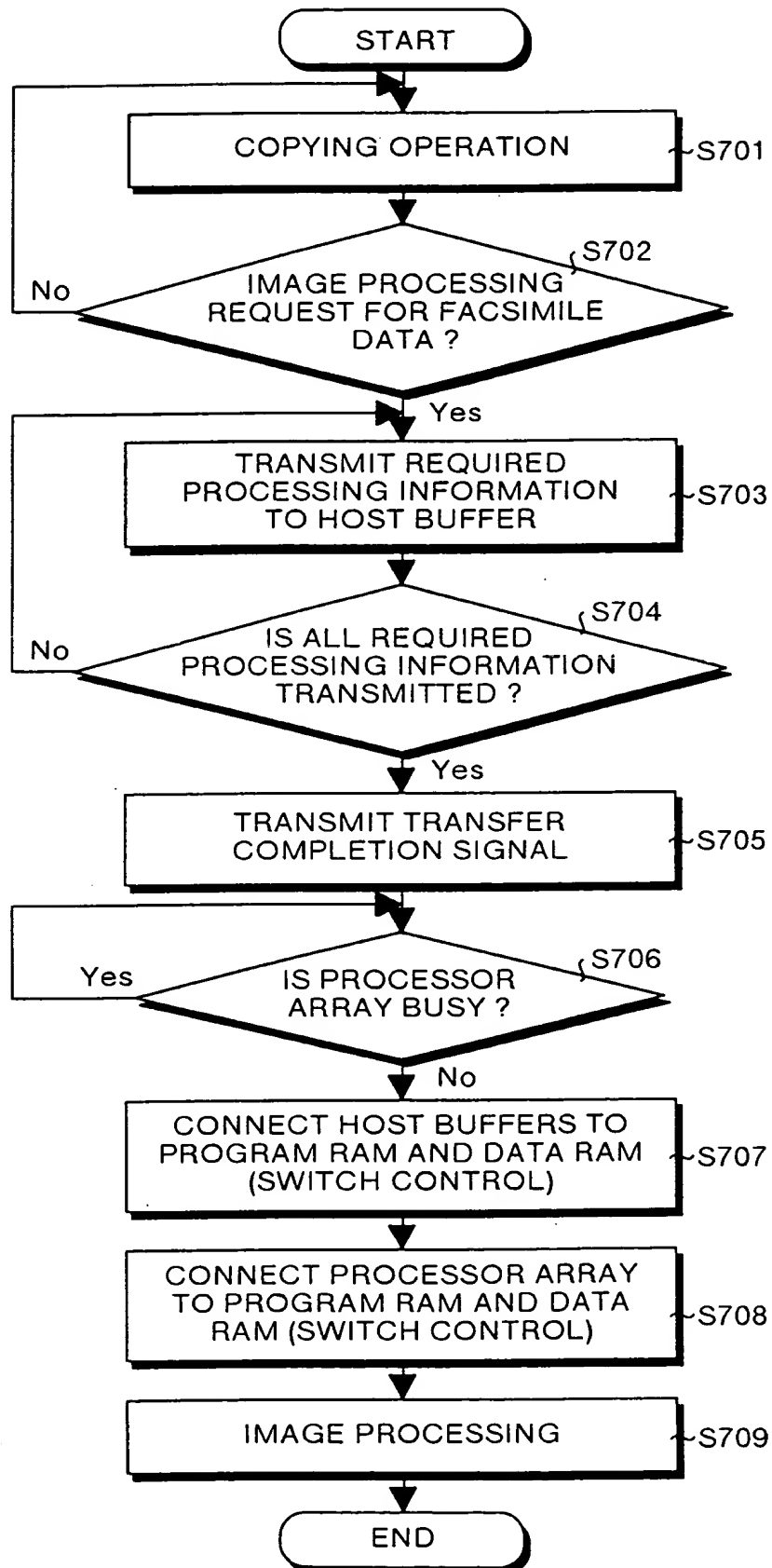


FIG.6



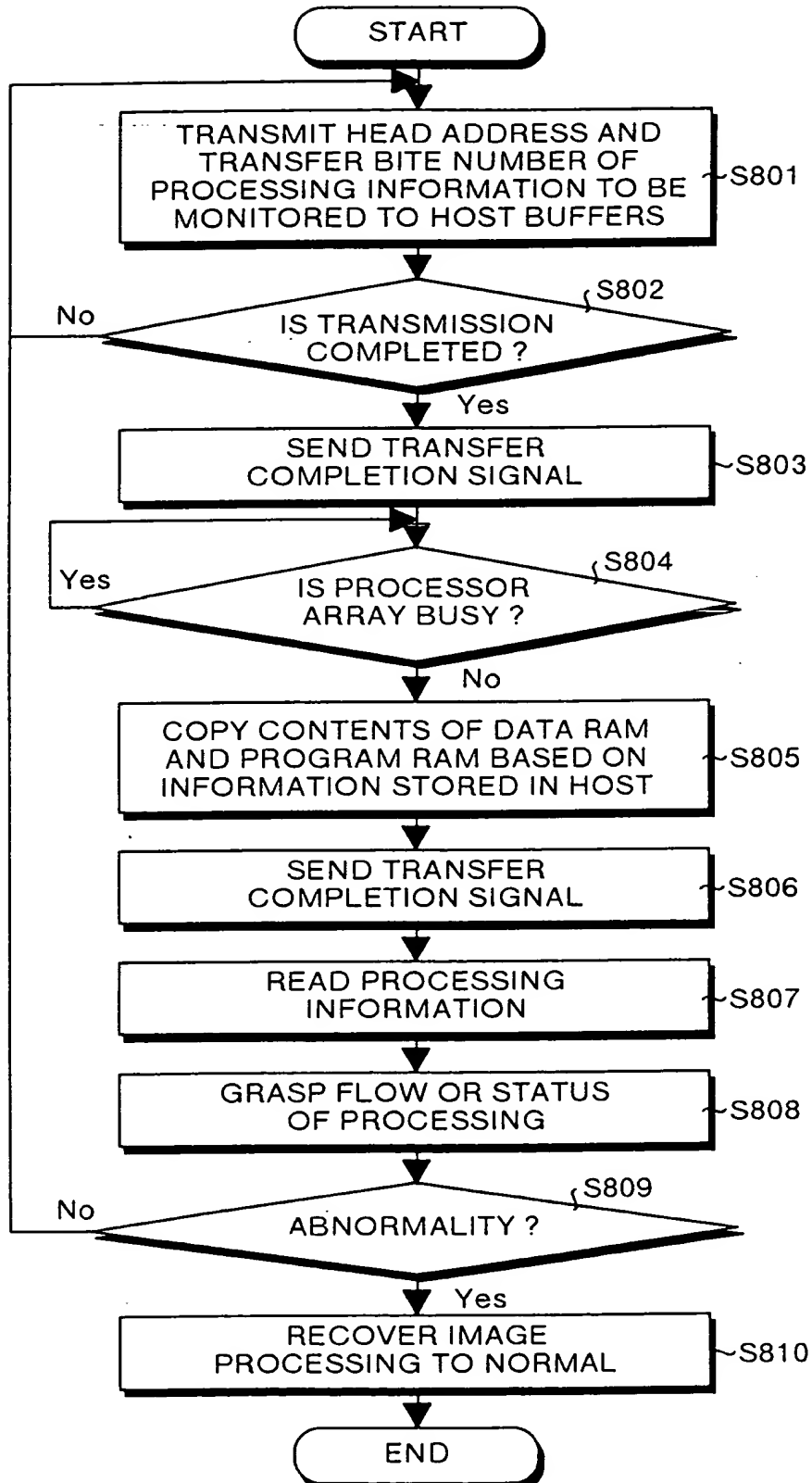
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FIG.7



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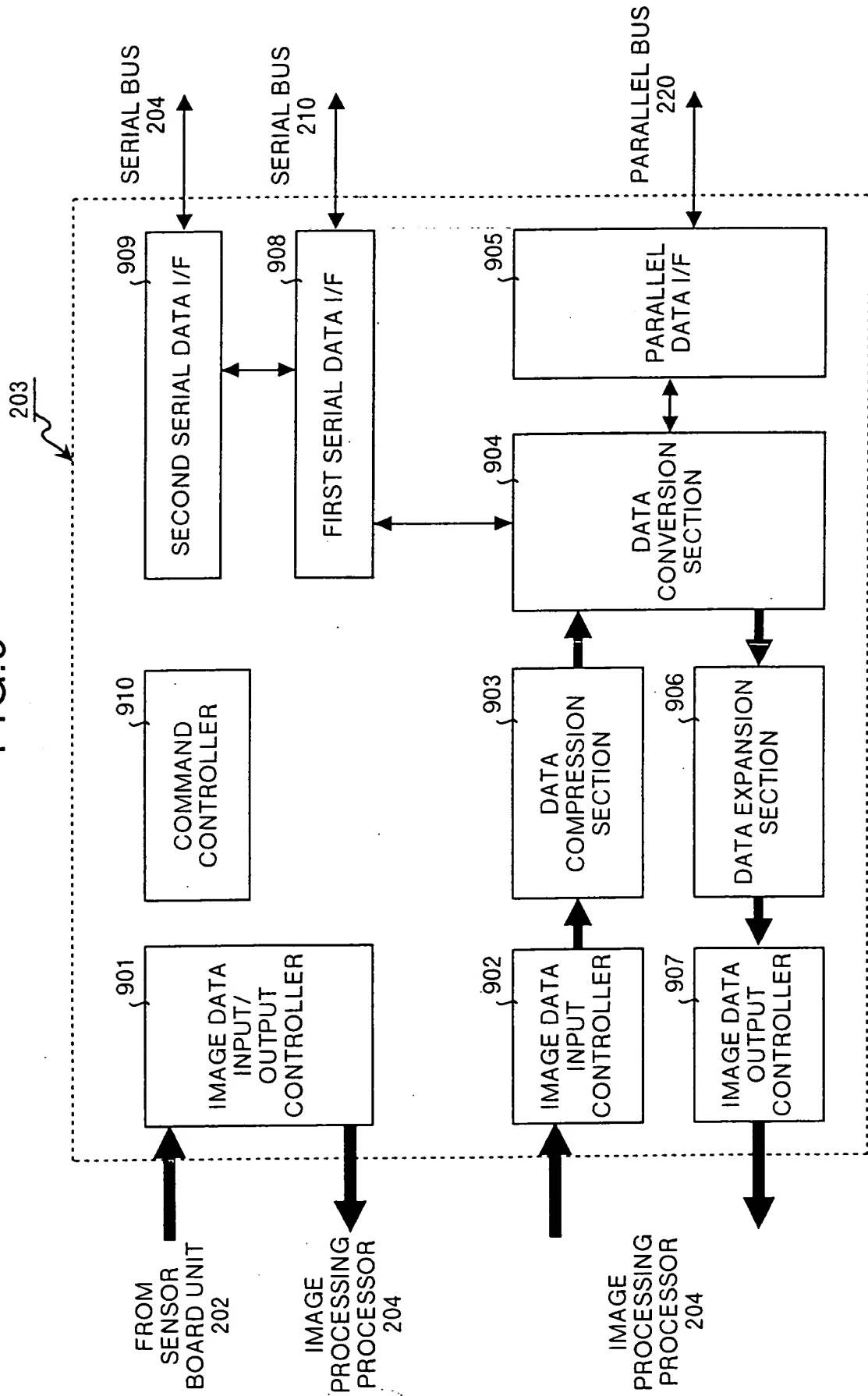
FIG.8





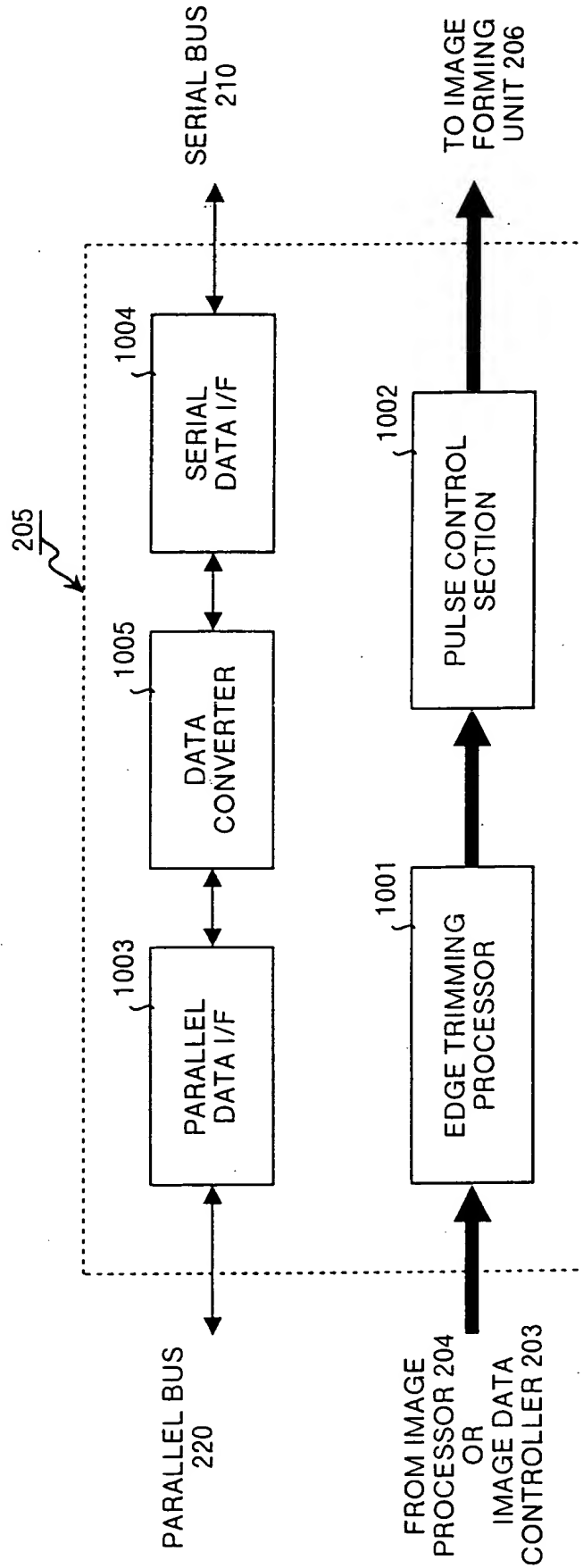
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FIG.9



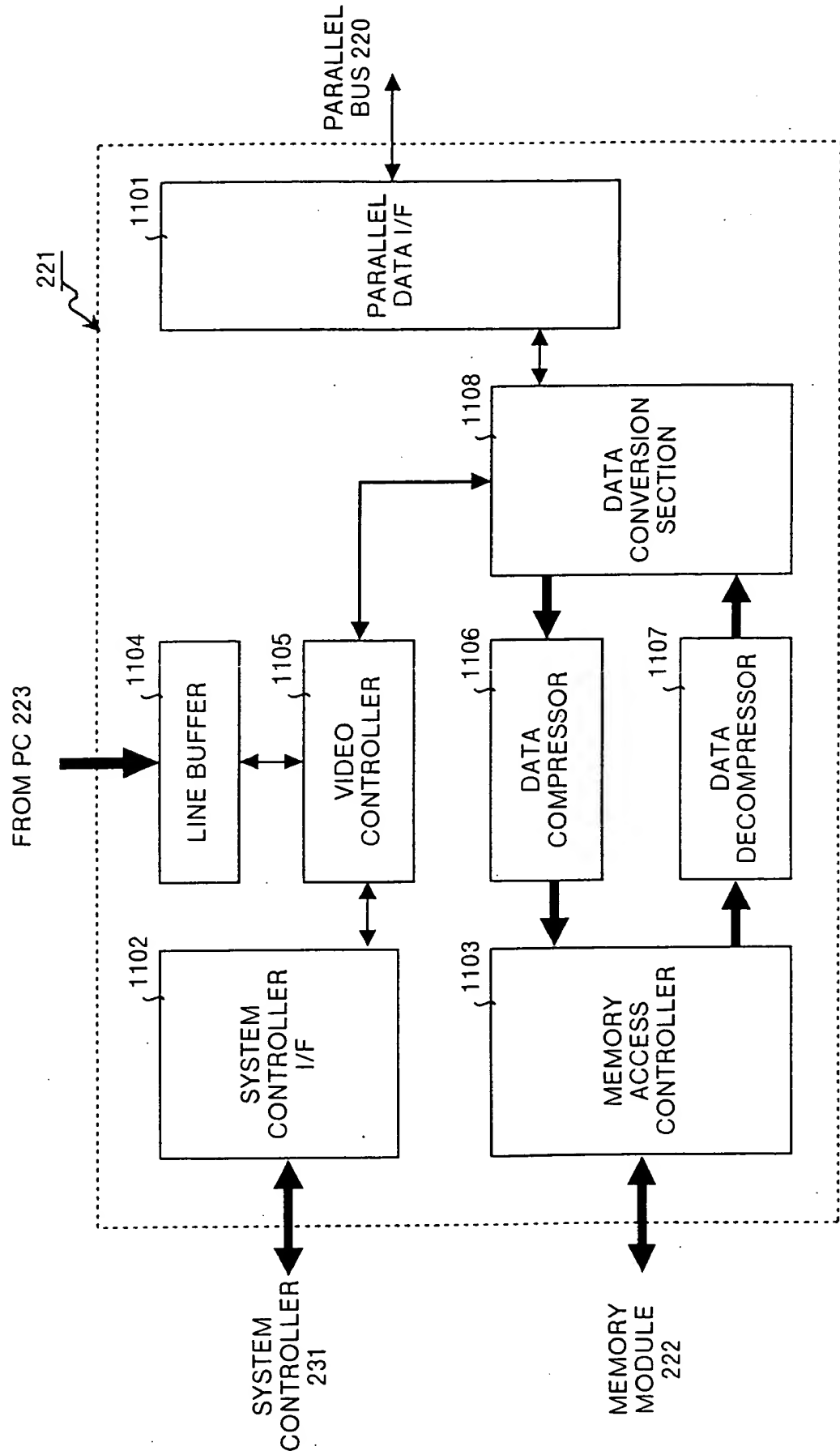
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FIG.10



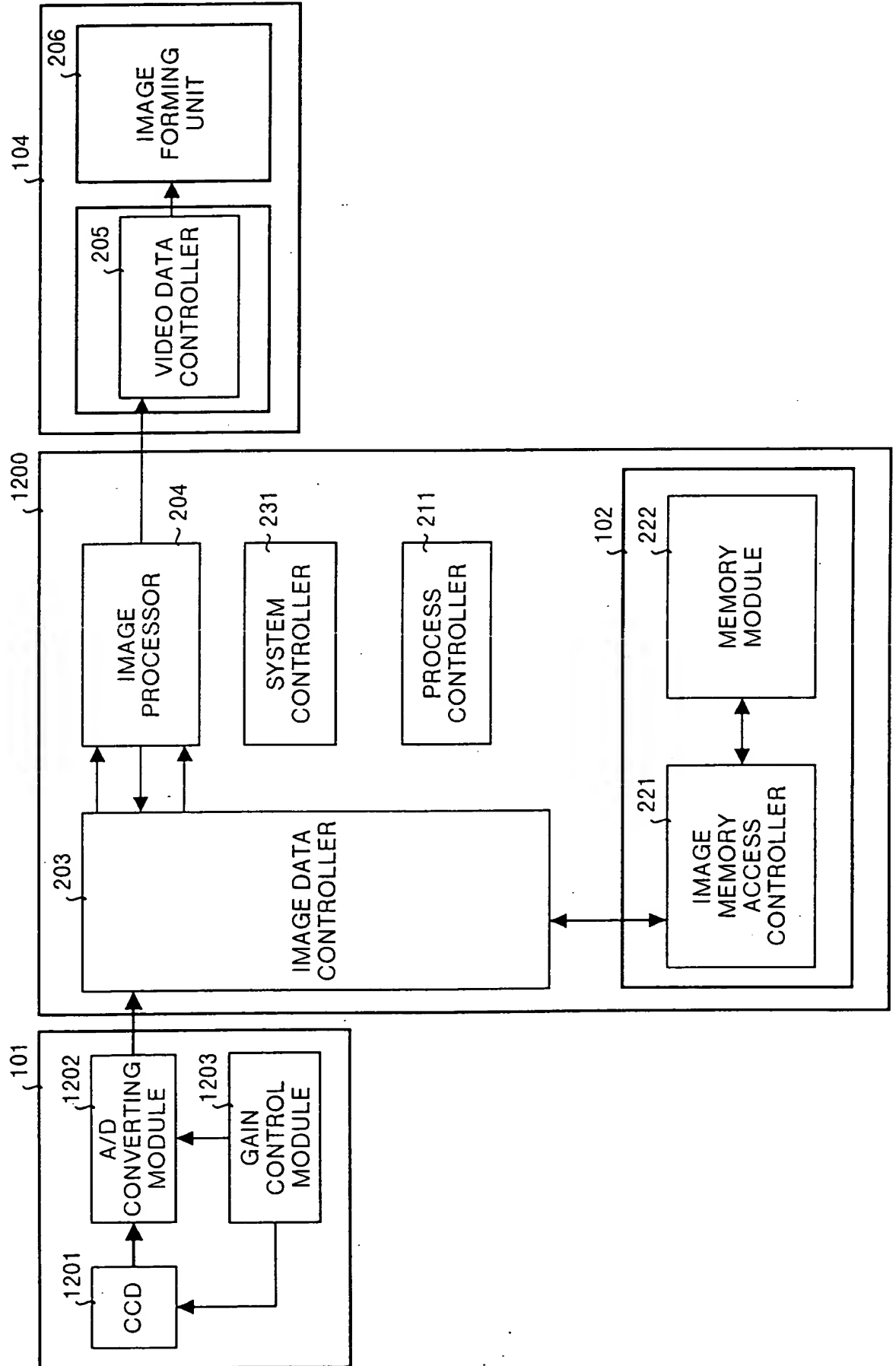
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FIG.11



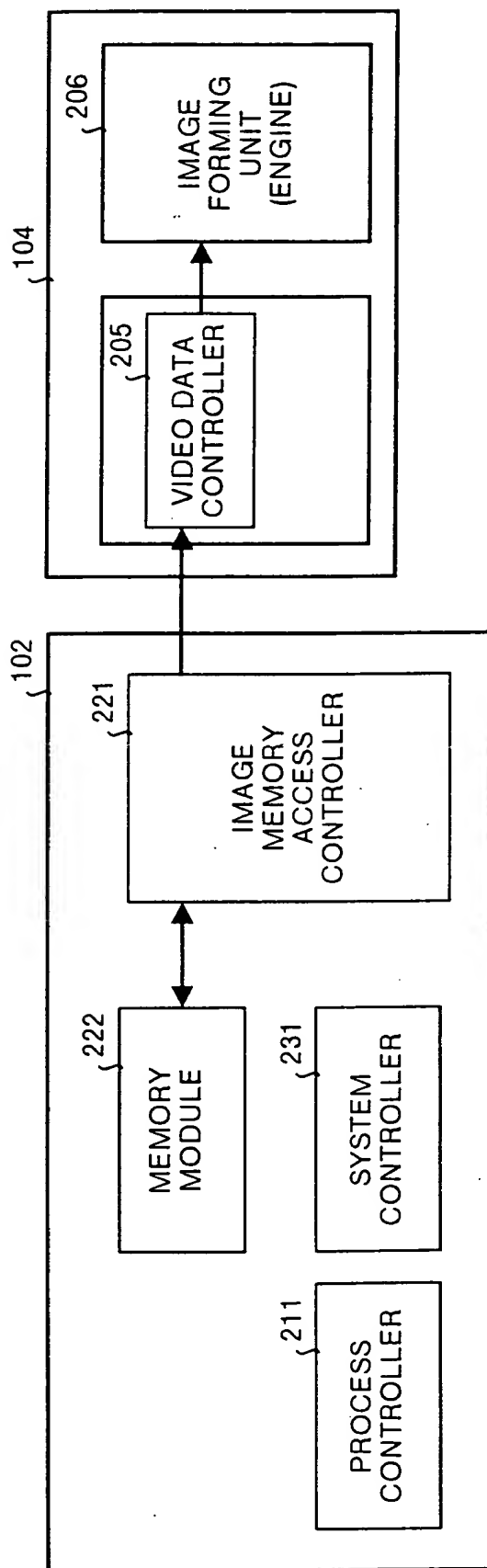
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FIG.12



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FIG.13



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FIG.14

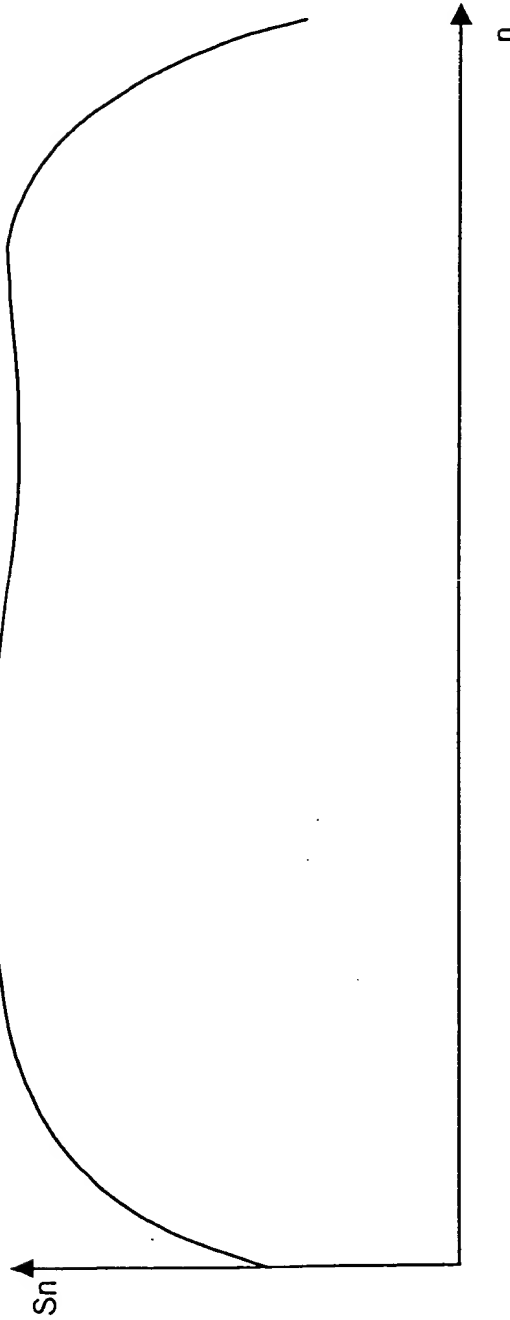
		COLUMN				
		j-2	j-1	j	j+1	j+2
ROW	i-2	A	B	C	D	E
	i-1	F	G	H	I	J
	i	K	L	M	N	O
	i+1	P	Q	R	S	T
	i+2	U	V	W	X	Y

FIG.15

S DATA							Sn					
D DATA							Dn					
C DATA							Cn					

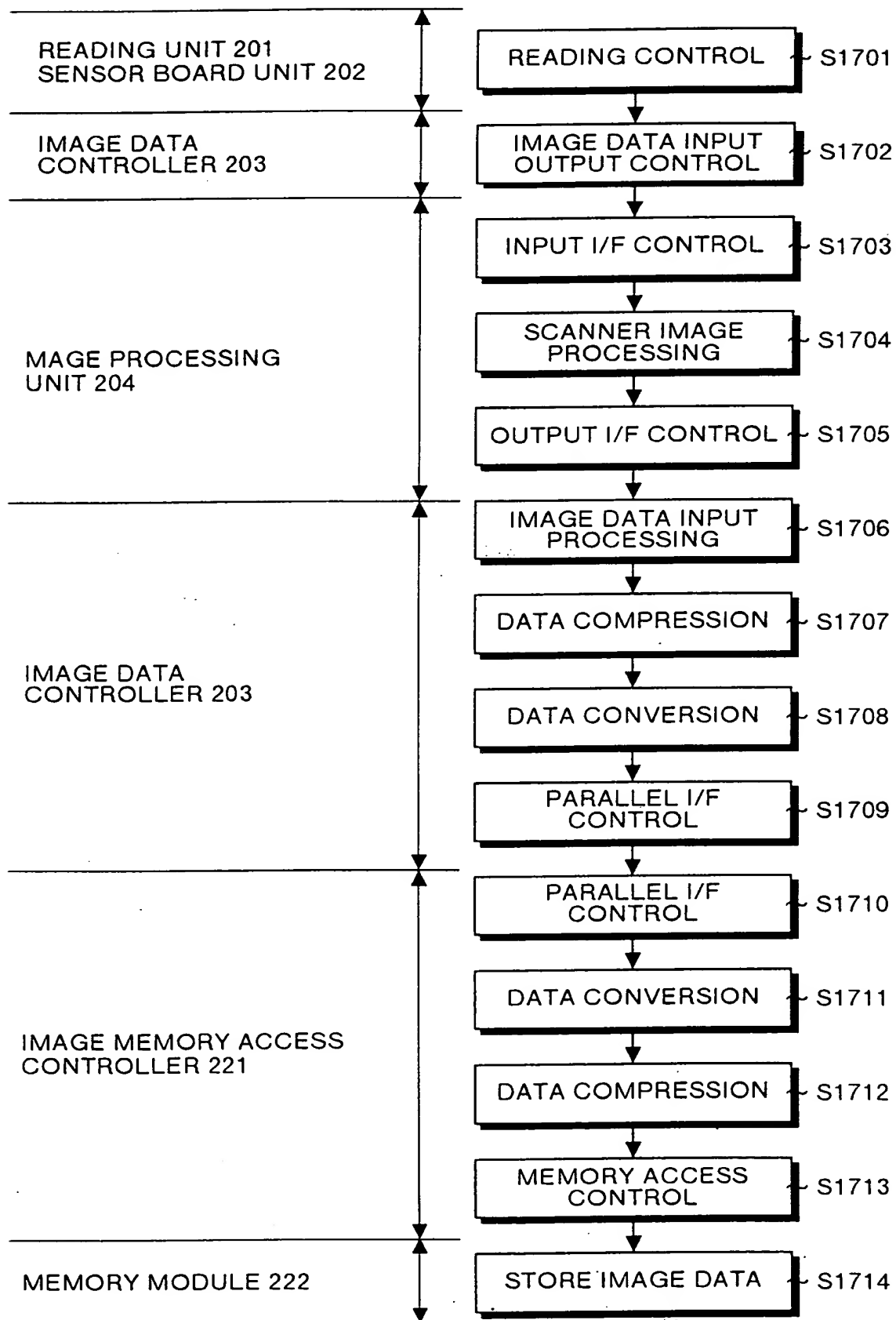
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FIG.16



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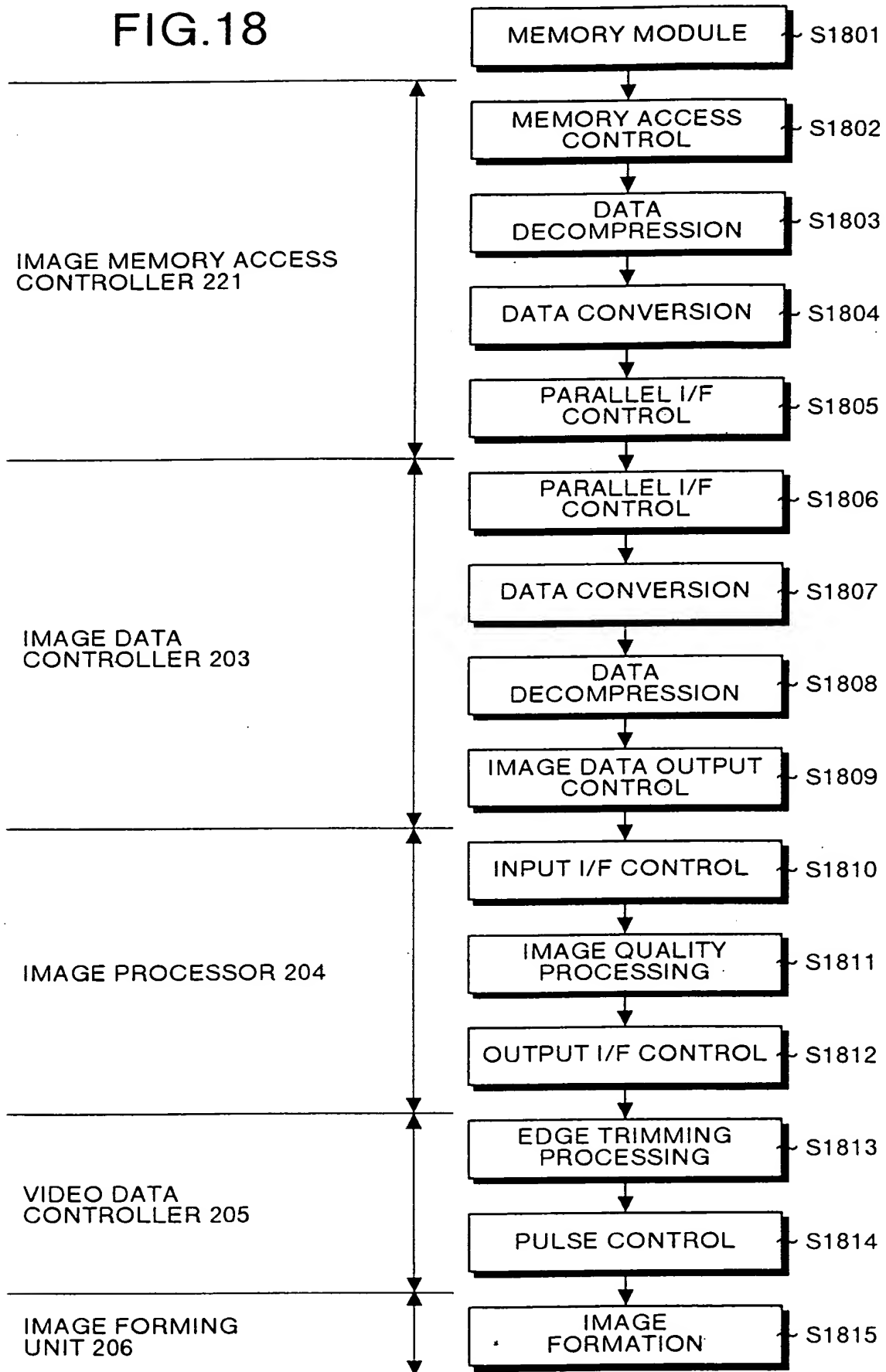
FIG.17





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FIG.18



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FIG.19

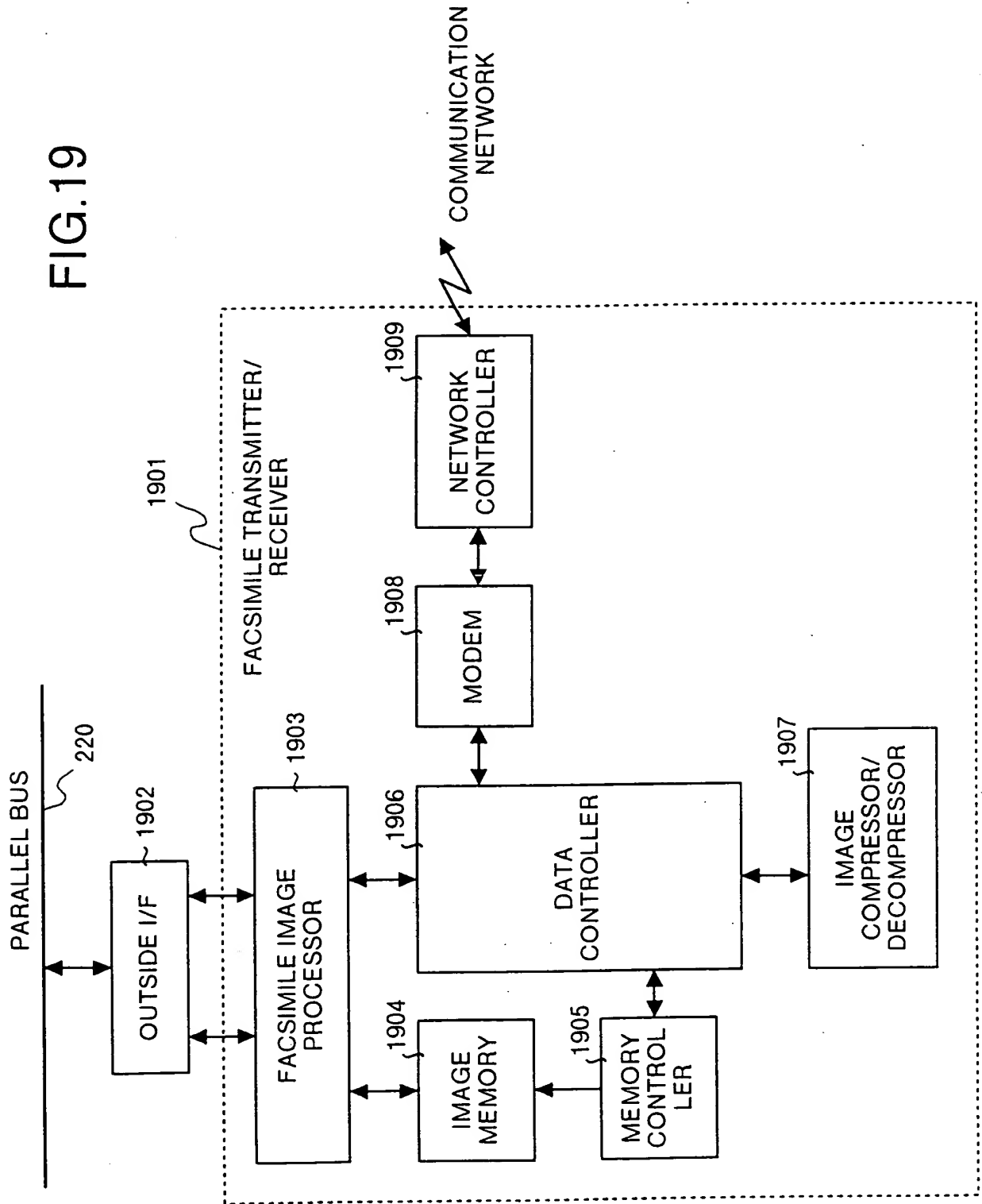
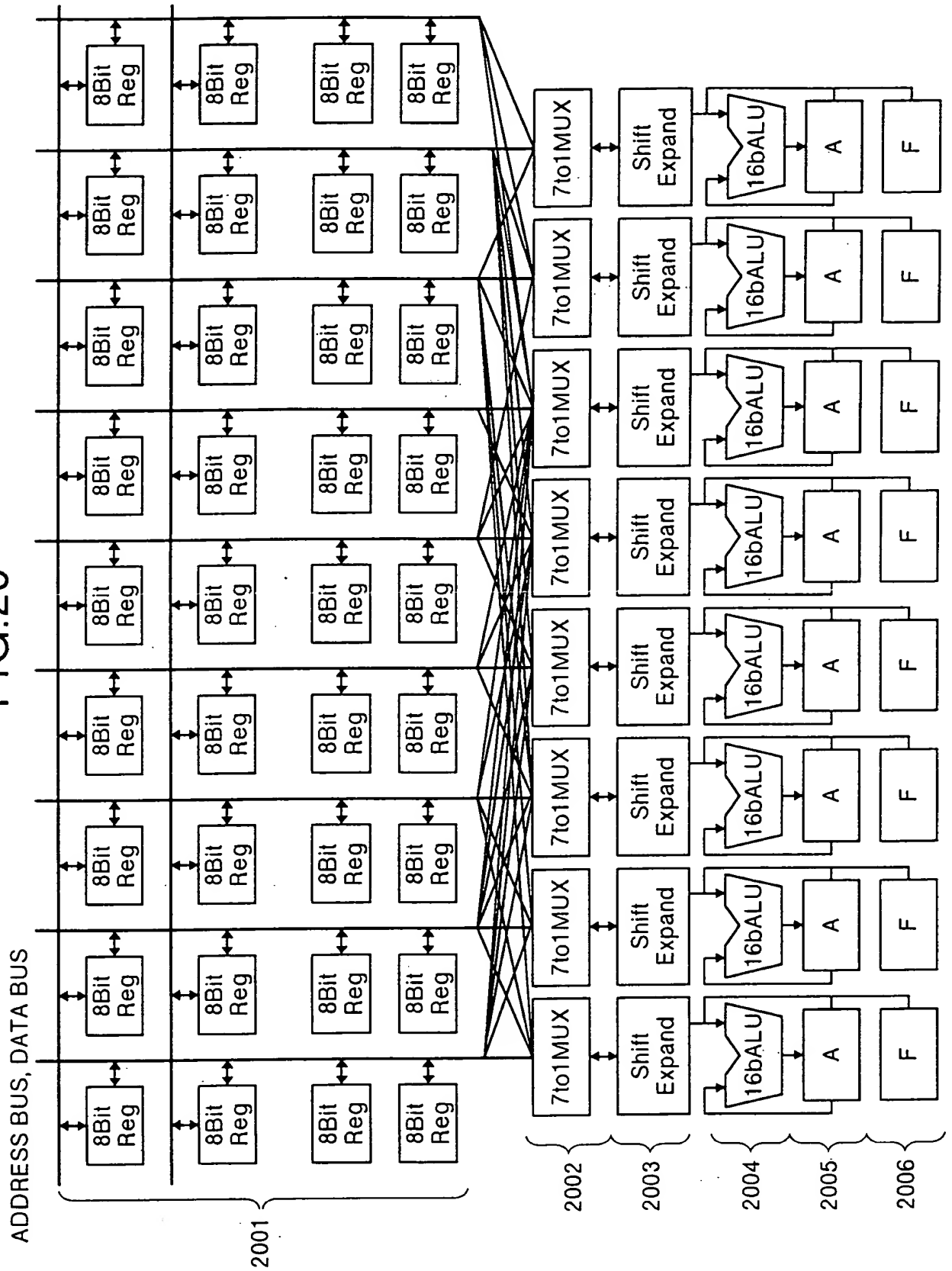


FIG. 20



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FIG.21

